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Docket No.: TESSERA 3.0-236 DIV

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Beroz et al.

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: Concurrently Herewith

Examiner: Not Yet Assigned

For: ASSEMBLIES FOR TEMPORARILY

CONNECTING MICROELECTRONIC

ELEMENTS FOR TESTING ANDMETHODS

THEREFOR

MS Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference was of record in Application No. 09/802,834, the benefit of which is claimed under §120. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Application No.: Not Yet Assigned Docket No.: TESSERA 3.0-236 DIV

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: February 12, 2004

Respectfully submitted,

Michael J. Doherty

Registration No.: 40,592

LERNER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK, LLP

600 South Avenue West

Westfield, New Jersey 07090

(908) 654-5000

Attorney for Applicant

LD-459\

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300	Stitute for form 144374 Bir 1	•		Application Number	Not Yet Assigned
IN	IFORMATION	I DI	SCLOSURE	Filing Date	Concurrently Herewith
STATEMENT BY APPLICANT				First Named Inventor	Masud Beroz
	(Use as many sheets as necessary)			Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	TESSERA 3.0-236 DIV

	U.S. PATENT DOCUMENTS					
Examiner	Cite No.1	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant	
Initials*		Number-Kind Code ² (if known)	MM-DD-YYYY		Figures Appear	
	AA	US-5,148,266	9/15/92	Khandros et al.		
	AB	US-5,915,752	6/29/99	DiStefano et al.		
	AC	US-5,518,964	5/21/96	DiStefano et al.		
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	AQ	US-6,342,443	01/2002	Tao et al.		

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Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶	
	BA						

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium; catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
-	CA	Multi-Chip Module Technologies and Alternatives; the Basics (Doame and Franzon, eds., 1993, pp. 450-476).			
	СВ	Harper, "Electronic Packaging and Interconnection Handbook", Mcgraw-Hill, Section 5.2, 1991			

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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